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1. Introduction

In the past decade the usage of solar energy production has increased rapidly and has become a reasonable alternative to fossil energy sources. Solar energy conversion is a challenge of the millennium and solar cells are one eagerly thought way of converting solar energy into electrical energy. Solar cells are divided into three generations according to Martin Green [1]: The 1st generation (1st gen) is till to date dominating production and installation and makes use of bulk, essentially silicon wafers with planar cell geometry. Today, a typical silicon based cell uses 200µm thick Si wafers as substrates. The 2nd generation (2nd gen) uses thin-films rather than bulk wafers and materials can be widespread. The idea is to use only very thin layers of photovoltaically active material on a passive mechanically supporting material e.g. glass. Such cells offer the option to use cheap substrates and even flexible substrates [2] so that cost saving potentials as well as new applications can be envisaged. Until today, thin-film cells (2nd gen) do not live up to their full potential due to the fact that they are 50%-60% less efficient than wafer cells (1st gen). Solar cell concepts termed 3rd gen aim at the combination of low production costs and high efficiencies. In order to reach this goal, unique optical and physical properties of complex nanostructures as well as novel materials and compounds are exploited. Silicon nanowires (SiNWs) are a very promising candidate for next generation PV. The basic material is abundant and cheap and the semiconductor industry gained intense experience with its handling. The nanostructuring and cell preparation can also be performed in cheap and large scale processes. Thus it could easily be integrated into existing PV lines.
1.1. Reasons for nanowires in PV concepts

The conversion efficiency of solar cells is affected by many factors. The internal quantum efficiency is already quite good for a long time and thus the industry concentrated more on optimizing the contacts for efficiency enhancement or reducing the overall costs. Whereas one efficiency record follows another in research [3], industry scale solar cells, however, are only improving slowly [4]. While optimizing factors like contact resistance or quantum efficiency only permit a moderate overall efficiency enhancement, the optical absorption is a property that can encourage large improvements of the SC efficiency. By using silicon nanostructures, the absorption of sunlight can be improved from typically 60% to over 90% in the wavelength range between 400 and 1100 nm [5–9]. This increased absorption is very encouraging and makes the nanostructures so interesting for SC research as well as for future industrial production.

One of many ways in order to reach this increased absorption of sunlight is the use of nanowires constituting the SC absorbers. The nanowires act as light trapping specimens due to its strong scattering capacities [10] and for the same reason as antireflective coatings [11–13]. A lot of different methods exist that produce such highly absorbing nanostructures which are divided into bottom-up growth mechanisms [14–16] and top-down etching processes [17–20]. This work will concentrate on a metal assisted wet-chemical etching (MA-WCE) process that does not need any vacuum equipment and can be performed even on large surfaces in a few minutes.

The application of micro- and nanowires in SCs is quite a new field of PV research. Having used the “Web of Knowledge” search engine, graphs were generated which are presented in Figure 1. From the first publication in 2002 [21], the topic has rapidly increased in popularity and it has reached nearly 250 publications in 2011. The number of citations also shows a growing increase of nearly 5000 citations in 2011.

Figure 1. Publications and citations on nanowire-based solar cells in the last 10 years.

1 http://www.webofknowledge.com/ Search properties: title includes (nanowire OR nanorod OR microwire OR micro‐rod) AND (solar)
2. Experimental part

2.1. Top-down fabrication of silicon

SiNWs can be produced by many different methods, normally divided into bottom-up growth mechanisms and top-down etching processes. While growing of SiNWs normally is conducted by using the vapor-liquid-solid (VLS) mechanism [14], which needs high temperatures, toxic gases and expensive vacuum equipment, top-down etching can be performed in easier ways. One differentiates between physical (dry) and chemical (wet) etching. Physical or dry etching is usually conducted in reactive gases offered in a reactive ion etching (RIE) setup [22], while chemical etching is usually based on wet chemistries, preferably on the basis of hydrofluoric acid for the case of silicon. Both methods have assets and drawbacks, but when it comes to the question of fast and cost-effective fabrication with the perspective of mass production, wet-chemical (WC) etching is intriguing.

Since details of WC etching processes were already discussed in a previous Intech book chapter [20], this work will only give a short summary and point out the latest optimization processes. The SiNW substrates that constitute the SC absorber are fabricated in a two-step MA-WCE procedure. Therefore, the substrate material, which is typically a lowly doped (1-5 Ohmcm) n-type Si wafer, passes four different etching solutions. In the beginning, the native oxide is removed with 2% hydrofluoric acid (HF, solution I). In the following step (solution II), silver (Ag) nanoparticles are formed on the surface of the wafer in a mixture of 0.02 molar (M) AgNO$_3$ and 5M HF in a ratio of 1:10. The duration of this step influences the density of particles and thus also the forming SiNW geometry. This procedure needs to be less than 30 seconds. Otherwise, the Si is completely coated by a continuous Ag layer and no access of the etching solutions to the Si surface can be achieved. Controlled SiNW properties can be achieved when using the following etching steps. Solution III consists of a mixture of 5M HF and 30% H$_2$O$_2$ in a ratio of 1:1. In this step the Si is oxidized by the interplay of H$_2$O$_2$ and the nanoscale Ag particles that form the thermodynamically unstable intermediate compound AgO(OH) which is a very efficient oxidant that oxidizes the silicon beneath the Ag nanoparticles. The resulting SiO$_2$ is dissolved by the HF in the solution. Thus, the Si is removed below the Ag particles which dig into the Si wafer and thus move away from the wafer surface inducing the SiNW formation. The direction of this Ag nanoparticle movement is anisotropic following certain crystal orientations and depends on the temperature as well [20]. Under controlled conditions, the SiNWs form perpendicularly to the surface of a Si(100) wafer and are oblique to the surface of a Si(111) wafer following <100>-directions [18]. In the last step (solution IV), concentrated nitric acid is used to remove the remaining Ag nanoparticles. Figure 2 illustrates the etching steps process.

During the SiNW formation, an increasing absorption can be observed after a few seconds. The shiny Si wafer surface turns matt finished and changes to a brown color which becomes deep black after one minute of etching. After three minutes the SiNWs reach a length of 1µm and elongate with a rate of 1 µm/min after that.
Figure 2. The WCE process is based on a sequence of four solutions starting with oxide removal (solution I: dilute HF, not shown), followed by solution based Ag nanoparticle deposition (solution II), Ag nanoparticle assisted catalytic etching of Si (solution III) and Ag nanoparticle removal (solution IV).

2.2. Fabrication of Semiconductor-Insulator-Semiconductor (SIS) solar cells

SiNWs enable several concepts of efficient solar cell geometries. The charge carrier dividing junction (p-n or Schottky type) can be applied in a radial [23,24] or axial [25,26] concept inside the NWs and a possible solution is also the wrapping of NWs with a second material [27]. In this work, we used a semiconductor-insulator-semiconductor (SIS) heterojunction solar cell concept that has already been developed in the 1970s [28–30]. Therefore, the complex SiNW structure is uniformly coated with a very thin tunneling barrier and a thicker layer of aluminum (Al) doped zinc oxide (ZnO) as a second, transparent wide band gap, degenerately doped semiconductor. In order to achieve this conformal coating, atomic layer deposition (ALD, Oxford Opal) is used. This technique can deposit many materials in a self-limiting process with a thickness control at the Angstrom level even on highly complex surfaces [31,32]. Two different classes of precursors are needed for the ALD deposition of metal oxides like Al$_2$O$_3$, TiO$_2$ or ZnO: metal containing molecules and an oxidizing agent. The two precursors are pulsed into the ALD vacuum chamber one after the other by using very short pulse durations of typically <100ms, while longer inert gas purging steps are used between the precursor pulses to remove residual gas from the chamber. The special advantage of an ALD process is that the precursors are forming a self-limiting monolayer that does not allow further deposition of that precursor. The oxidizing agent can be water or an oxygen plasma which removes the functional groups of the previously deposited metal precursor molecules and forms the metal oxide [33,34]. This step is also self-limiting so that a completed cycle forms a stoichiometric monolayer of the desired material even on 3D surfaces. In the case of Al$_2$O$_3$, trimethylaluminum and oxygen plasma were used for its formation and for ZnO deposition, diethylzinc and water pulses are applied. The doping of ZnO with aluminum was realized by substituting every 20$^{th}$ cycle of ZnO by one cycle of Al$_2$O$_3$ [35,36].

Two different insulating layer materials were tested to form the barrier layer in the SIS cell: TiO$_2$ and Al$_2$O$_3$. Depending on the material, 10 to 30 Å of the barrier layer were deposited onto the SiNWs. Subsequently, the second semiconductor, Al doped ZnO (AZO), which acts as transparent front contact at the same time, is deposited in the same ALD reactor. The thickness of this layer was typically around 450 nm which resulted in a complete filling of the gaps in between the SiNWs and also created a continuous AZO layer on top of the SiNWs for efficient charge carrier conduction. Figure 3 shows the three steps of the ALD process.
Figure 3. Schematic of the layout of an SIS solar cell concept based on SiNWs: etched SiNWs (left) are coated with a thin tunnel barrier (middle) and subsequently with a transparent front contact – Al doped ZnO (right).

The structural analysis of the completed SIS solar cells was performed by scanning electron microscopy (SEM) techniques including 3D analysis supported by a sequence of focused ion beam (FIB) etching steps. Thereby, the gap filling between SiNWs and SiNW topologies were investigated. Figure 4 shows two colored cross section micrographs of as etched SiNWs and of a completed SIS solar cell that contains wrapping layer stacks on the SiNWs of barrier layer and AZO.

Additionally, the complete SCs were analyzed by glow discharge optical emission spectrometry (GD-OES, Horiba Jobin Yvon JY 5000 RF) to obtain a depth profile of the compositions of the material [37,38]. In a GD plasma source, cathodic sputtering is used to remove material layer-by-layer from a sample surface with an accuracy of a few nanometers [39]. The sample atoms are transported into a plasma plume, in which they are excited and ionized through collisions with electrons, ions and the metastable carrier gas (here: Ar).

Figure 4. Left: colored SEM micrograph of as etched SiNWs. Right: colored SEM micrograph of a FIB polished cross section of a completed SIS solar cell. The SiNWs (blue) are embedded in barrier layer and Al:ZnO (AZO) (orange).

The analyte isotopes of interest can then be detected via downstream spectroscopic techniques. In the presented study, OES is performed by analyzing the intensities of characteristic emission lines of excited analytes with a spectrometer (typically a Paschen–Runge monochromator or Czerny–Turner polychromator) with A GD-OES Such an analysis was performed on a SiNW SC with a TiO₂ tunnel barrier in the optimization of the solar cell production process. In the SEM profile in Figure 5, the SC can be divided into three regions:
region 1 shows the front contact which solely consists of aluminum doped zinc oxide (AZO). The second region is composed of the SiNW array with a wrapping TiO$_2$ layer which is filled with AZO. In the third region, the bulk Si wafer is reached. From the measured spectra several interesting information can be extracted [40]. In region 1 the Al content is found to be 2.5 mass% (m%) leading to an ideal conductivity of the AZO layers. In region 2, the silicon content increases only slowly in depth which leads to the assumption that the wires are tapered. This can be interesting for further theoretical investigations regarding optical simulations of light trapping effects. After having checked the residual silver, it is revealed that the cleaning process of the nitric acid did not completely remove the metal. According to these findings, the cleaning process was improved to eliminate residual silver contaminations.

The GD-OES measurements enable another interesting analysis of the silicon nanostructure: Knowing the total mass and thickness of the tunnel barrier layer, the surface of the nanostructures can be calculated. Thereby, the surface enhancement of the etched nanostructures as compared to a flat wafer can be derived. In the case of the TiO$_2$ barrier SiNW SC (Figure 5), an enhancement factor of 6.2 was calculated. This factor is strongly dependent on the etching conditions and can easily reach values of 40 and more. For a solar cell, a higher surface amount is a clear disadvantage because higher junction areas result in higher surface recombination of charge carriers. On the other hand a higher surface increases the absorption of light and leads to an overall higher charge carrier collection at the interface. Thus, an optimum has to be found between the counteracting mechanisms.

**Figure 5.** Top: SiNW SC cross section and scheme, showing three different regions inside the cell. Bottom: GD-OES spectra illustrating the depth dependent mass% of the elements Si, O, Zn, Al, Ti and Ag. Adapted from [40].
During the research state, typical SCs had only a size of a few mm² and thus did not need metal front contacts. In the later course of cell development, first demonstrators that had the size to power electrical devices were needed. Therefore, two different prototypes with gold front contact grids and sizes of 36 cm² and 1.4 cm² were fabricated (see Figure 6). To demonstrate the functionality of the SCs, the small demonstrator was contacted to a motor with a rotating wheel and was illuminated by a battery powered flashlight. Even though the flashlight delivered only a fraction of the sunlight’s power, the SC produced enough energy to drive the motor.

![Figure 6](image)

**Figure 6.** Left: SiNW SC prototypes with gold front contacts. The large cell has a size of 36 cm², the small cell measures 1.4 cm². For comparison: a 10 €-cent coin. Right: The 1.4 cm² cell under a battery-driven flashlight produces enough power to rotate a small toy device.

3. Characterization

The characterization of our fabricated SCs can be divided into investigation of structural and electro-optical properties. Understandably, the conversion efficiency is the most important property. It is determined in a current-voltage (I-V) measurement under a solar simulator that reproduces the certified AM 1.5 solar spectrum. An optical analysis was performed with an integrating sphere in order to investigate the connection between light absorption and efficiency. Figure 7 shows the measured photocurrent under illumination at -2V of 12 different solar cells and the respective optical absorption (mean value between 400 – 1100 nm). In most cases a connection between these two values is clearly visible. Therefore, it is crucial to optimize the absorption by tailoring the nanostructured material composite. Furthermore it can be seen that even a small reduction in absorption from 90% to 88% (wafer 1 and 4) can cause a serious drop in photocurrent from 35 mA to 15 mA. Although other effects may play a role, this effect cannot be neglected.

After these findings the best solar cells were analyzed in detail regarding energy conversion and quantum efficiency as well as local conversion properties in terms of light and electron beam induced current mappings (LBIC/EBIC).

Over 120 solar cells with three different types of Si wafers and 7 different etching conditions were fabricated. For the record cell a n-type Si(111) wafer with a doping of 1-9 Ohm*cm was
used that was etched for 30s/6min in the two etching solutions. The Al$_2$O$_3$ tunneling barrier had a thickness of 12 Å. From this cell a small piece of 8mm$^2$ size was analyzed in the solar simulator. The SC shows a clearly visible diode-like behavior in the dark with a rectification factor of 6600 at +/-2V. Under AM1.5 illumination the record cell reaches an open circuit voltage $V_{OC}$ of 453 mV, a short circuit current density $J_{SC}$ of 35 mA/cm$^2$ and a fill factor (FF) of 57%. This results in a conversion efficiency of 9.1% which is an impressive result for a nanowire based solar cell with as little optimization as we did, e.g. in terms of surface passivation [41,42] or size and shape of the SiNWs [22,43,44]. The corresponding dark and illuminated J-V curves are shown in Figure 8.

Figure 7. Measured photocurrent (at -2V) and absorption of 12 different solar cells. A correlation is clearly visible.

Figure 8. Dark and illuminated current density – voltage (J-V) curves of our record SiNW-SC out of 120 test samples. Very high $J_{SC}$ combined with average $V_{OC}$ and fill factor yield in a conversion efficiency of 9.1%.
3.1. Local electro-optical properties

To determine the influence of structural inhomogeneity on the electrical properties, it is necessary to generate localized charge carriers inside the solar cell and perform a mapping across the cell while monitoring the generated external current between front and back contacts. Two different methods enable such an analysis: light and electron beam induced current (LBIC [45] & EBIC [25, 46]) measurements. In LBIC, a laser is scanned over the solar cell surface and the generated current is measured for each spot. In this way an efficiency mapping can be obtained. By using lasers with different wavelengths the response of the solar cell to distinct excitation energies can be investigated. Furthermore, an external quantum efficiency (EQE) mapping can be calculated from such a measurement. The disadvantage of LBIC is its large laser spot focus which inhibits the analysis of µm-sized defects. Here EBIC becomes suitable. The electron beam of an SEM can be focused on a few nm and with a low acceleration voltage the expansion of excited charge carriers can be kept at a minimum of <100nm. Every incident electron generates thousands of charge carrier pairs that can be separated by the nearby SIS junction. This enables the visualization of the internal charge carrier separation at the SiNW - front contact interface.

The LBIC and EBIC analyses were performed on the 1.4 cm² prototypes with metal front grid shown in Figure 6. For the LBIC measurements, a custom built system with four different lasers with wavelengths of 405, 544, 633 and 1064 nm was applied. Figure 9 shows the mapping results and an EBIC mapping of the same cell as well in order to be able to draw a comparison. The LBIC mappings are normalized to show the dependence of the power output on the light wavelength. Nearly no current is generated at 405 nm. At 544 nm, several areas are visible that have better or worse efficiency. These areas arise from an inhomogeneous etching process which generates areas with slightly higher or lower absorption. At 633 nm two small areas reach a generated current of 25 µA. Visible for the naked eye, these areas appear darker than the remaining surface. At 1064 nm, the light is not absorbed in the SiNWs themselves anymore but in the bulk wafer below. Therefore the signal is very homogeneous as the nanowires do not influence the current generation anymore. The EBIC mapping can be compared best with the 633 nm LBIC mapping. Due to the higher resolution of the EBIC, this measurement shows much more details.

In order to explain the different current amounts in the LBIC measurements in a better way, the EQE of the SC was measured. The results are shown in Figure 10. The wavelength-dependent behavior is in accordance with the determined LBIC mappings. At 405 nm, the EQE is at nearly zero and 633 nm exhibits the highest EQE from all four laser wavelengths. Although the EQE curve is similar in shape to typical commercial solar cell EQE results, only lower values are reached. The reasons are still under investigation. Furthermore, Figure 10 shows the optical absorption of the SC which was calculated by 1 minus the measured total reflectance. In Figure 11, a high resolution large scale EBIC scan of a SC demonstrator is shown. The simultaneously gathered SEM and EBIC signals are coded in brown and green color schemes. The EBIC signal clearly shows regions of lower or higher current due to etching variations and also many circular defects which
seem to be the result of pinholes in the tunnel barrier layers. In several circular defects, a dirt particle in the middle seems to be the cause of the short circuit.

**Figure 9.** LBIC mapping at four different laser wavelengths and the corresponding EBIC measurement at 30 kV accelerating voltage. The inactive region in the top-right of each LBIC measurement is caused by shading from the mounting.

**Figure 10.** Absorption ($1-R_{\text{tot}}$) and EQE of a SiNW-based solar cell prototype.

The EBIC measurement is a powerful tool not only for observing the surface of a solar cell but also for looking inside the cell when combining sequences of EBIC analyses and FIB sectioning and polishing. Combined with a focused ion beam (FIB), information on sub-surface structures can be obtained. In a tomographic approach, a section into the SiNW based SC is
carried out and a sub-surface slice of a few µm in depth and length is polished. The prepared area can subsequently be investigated by EBIC mapping and typical SEM SE imaging. Further slicing and polishing and repeated EBIC analysis permit a 3D reconstruction of EBIC as well as SE data resulting in tomographic information. Figure 12 represents the concept of a cross sectional EBIC measurement: The electron beam scans over the ion beam polished cross section of a SiNW based SIS SC and the current that flows between the front and back contact is amplified and mapped by using the electron beam position. A resulting image is also shown.

![Figure 11.](image1.png) **Figure 11.** A large scale combined SEM/EBIC image of a solar cell surface. Point defects appear as darker dots and larger sized contrast differences indicate structural variations due to inhomogeneous etching.

![Figure 12.](image2.png) **Figure 12.** Left: Scheme of a cross sectional EBIC measurement of a SiNW based SIS SC. Right: colored dual-signal micrograph that shows the SEM image in red and the EBIC in green. A pronounced EBIC current of an individual SiNW is clearly visible in green.

The 3D-EBIC is devoted to solve an important question in the field of SiNW solar cells: Are the nanowires active photovoltaic components or do they only act as passive anti-reflecting building blocks? It is generally accepted that SiNWs absorb light by generating charge carrier pairs [10]. However, whether it is possible to separate electrons and holes prior to their recombination is a question to be answered for each and every material and cell concept.
again. To answer these interesting and important questions a dual beam SEM/FIB system (Tescan Lyra 3) with an integrated quantitative EBIC detector (Point electronic and Kammrath & Weiss) is used. The gallium ion beam of the FIB cuts a volume of 10x10x10 µm³ of the solar cell perpendicular to the surface slice by slice. The electron beam observes each slice from an angle of 55° to the perpendicular and creates a secondary electron image as well as an EBIC image. Afterwards, these images can be combined in order to create a 3D reconstruction of the solar cell. In Figure 13, three slices of a 3D reconstruction are displayed. The assembled dataset offers a multitude of visualizations which can be shown best in a video. Therefore, the microscopy manufacturer Tescan provides a video of the analysis of our SiNW-SIS-SC which can be viewed via this internet link: http://www.youtube.com/watch?v=X9O6pwcN4Tg. It shows the possibilities in signal combination as well as the visualization of complex 3D surfaces.

![Figure 13. Three single slices out of 400 that show the SE-signal (top) as well as the EBIC signal (bottom) at the same position.](image)

The 3D determination of electrical properties becomes very important in times of emerging nanostructured solar cells. Our results which are shown in Figure 12 and 13, prove that nanowires can be active photovoltaic components. This can be seen in the second slice where a single nanowire is visible in the EBIC image. Otherwise, many NWs are visible in the SEM image that cannot be seen in the EBIC signal. The difference between active and passive PV component seems to lie in the diameter of the wire. NWs, which are smaller than 50nm, exhibit no EBIC signal. That can be explained by the high surface to volume ratio, which leads to higher recombination, or a lesser absorption and thus no charge carrier generation occurs. These information are of great value when it comes to further optimization of the initial etching process. The nanowire dimensions should be in an area where high absorption is still achieved and charge carrier separation can take place inside the wires.
4. Outlook on nanowire research

The field of nanowire based solar cell research develops very fast and thus new methods and materials are coming up continuously. Our group is driving this type of research forward very actively and we are even constantly improving our own records. This outlook will give the reader some ideas about the progress of NW cell research in our lab. By further improving nanowire based cell concepts, we are aiming to achieve the goal of seeing these cells on top of a roof one day.

4.1. Silicon nanowire arrays formed by reactive ion etching

Reactive ion etching (RIE) in combination with nanosphere lithography (NSL) represents a new way of forming hexagonal arrays of SiNWs with controllable geometries in wafers [22] or even multicrystalline silicon (mc-Si) thin films on glass. [44] These structures offer several advantages regarding further systematic research on SiNW based optoelectronic and solar cell devices. Since device constituting properties like spectral absorption, reflection and the area and distribution of charge separating surfaces (p/n-junctions, heterojunctions etc.) are dependent on SiNW array geometries, a control of these geometries creates the opportunity for systematic device development. Figure 14 shows an example of the potential of this highly flexible and scalable process which results in large areas of nanostructured surfaces. The nanospheres arrange in self-aligned hexagonal colloidal 2D crystals on the wafer surface. The initial sphere diameter determines the lattice constant of the structure, whereas oxygen-plasma reduced spheres pre-determine the diameter of the SiNWs.

![Figure 14](image)

Figure 14. Left: RIE of a NSL patterned wafer surface. The nanospheres act as a shadow mask for the RIE process which results in aligned arrays of SiNWs. Inset: The lattice constant of the hexagonal arrays is pre-determined by the initial sphere diameter $d_i$, the diameter of the structures $d_r$ can be controlled by the reduction of the initial spheres. Right: With the described method, the structuring of large surfaces is possible as it can be seen in the SEM micrograph.

RIE etched SiNWs may not offer the high throughput and cheap production of WCE NWs, but the predefined non-random structure offers many advantages in the control of optical and electrical properties in NW SCs compared to the random structures of chemically etch-
ed NWs and thus permit tailoring and optimizing SC efficiencies. Moreover, the etched SiNWs usually have a smaller surface to volume ratio, thus permitting smaller surfaces that need passivation [41].

4.2. Novel electrodes based on silver nanowires or graphene

One-dimensional (1D) metal nanostructures, especially single crystalline nanowires, have attracted intensive interest because of their great potential for practical applications in nanoscale electronics, optoelectronics, and molecular sensing devices. However, one of the most significant challenges still facing device fabrication and processing is the large scale reproducibility of the nanowires into highly controlled arrays with high uniformity. Herein, we demonstrate an extremely simple method for large-scale and highly ordered silver nanowire (AgNW) monolayer synthesis via a thermal assembling process. These AgNWs offer new possibilities in the formation of transparent and highly conductive contacts.

The AgNW were synthesized by wet chemistry. For a typical synthesis, 5 ml of ethylene-glycol (EG) was heated to 175±10 °C for 1 h under magnetic stirring. Subsequently, Cu-additive solution (40 µL, 4 mM CuCl₂) was injected together with the polymer PVP (1.5 ml with a concentration of 0.147 M) into the heated EG. The volume, systematically influences can design the final shape and size of the AgNW. Finally, 1.5 ml of a 0.094 M AgNO₃ was injected slowly (for 10min). After 1 to 1.5 h, the reaction was stopped and cooled to room temperature by a water bath. Products were then washed with ethanol three times.

SEM images with low and high magnification of Ag nanowires (AgNW) are presented in Figure 15. The AgNW had a typical length of 30±10 µm, with diameter of 50±10 nm.

Figure 15. SEM micrographs of AgNW prepared by thermal assembly in the presence of PVP polymer. (a) A low-magnification SEM image reveals the highly uniform shape of AgNWs, (b) A higher magnification SEM image shows the high aspect ratio of AgNW. Inset is a SEM image that illustrates the pentagonal cross-section of the AgNWs.

Graphene with its outstanding optical and electrical properties offers many advantages when applied as contact material to SiNW SCs. The high in-plane conductivity as well as the high optical transparency recommends graphene as a new type of TCMs (transparent conductive materials) and it might substitute or support the typical TCO in our SiNW SC concept. It can be easily transferred to desired surfaces and its flexibility offers advantag-
es for flexible SCs (e.g. on plastic substrates or metal foils). Figure 16 shows a transferred layer of graphene on top of RIE fabricated SiNWs. Preliminary investigations strongly promise excellent contact properties and thus research on this material will be forced with a lot of effort.

![Figure 16. A graphene monolayer was deposited on top of SiNWs that are created by RIE to act as front contact. In the upper part the contact needle of the EBIC module can be seen.](image)

5. Conclusions and outlook

In the last years not only have we developed and realized a novel nanowire based solar cell concept, but we have also developed advanced analysis methods for such kind of nanostructured devices. By systematically using cheap and abundant materials and avoiding expensive vacuum equipment for the nanostructuring process, we generated solar cells with over 9% conversion efficiency. As these values are the results of early stage research, further improvement up to an efficiency of 12-15% are expected. Furthermore, the concept will be transferred to thin films like multicrystalline silicon on glass or flexible substrates.

The development of 3D-EBIC will be most likely of great influence for the analysis of 3-dimensional nanostructured solar cells and thus it is also a great advance for the PV research field.

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